# THC63LVDM83D / THC63LVDF(R)84B,84C Evaluation Kit 

LVDS Single Link Evaluation Board<br>Parts Number: THEVAM83D, THEVAF(R)84B, THEVAF(R)84C

## 1.General Description

THEVAM83D and, THEVAF(R)84B, 84C boards are designed to support video data transmission between the host and display. One high-speed lane can carry up to 24bits data and 3bits of synchronizing signals at a pixel clock frequency from 8 MHz to 160 MHz .

Table 1 Clock Frequency

| Type | Parts Number | TTL Clock Freq. |
| :---: | :---: | :---: |
| Tx | THEVAM83D | 8 MHz to 160 MHz |
| Rx | THEVAF(R)84B | 15 MHz to 85 MHz |
| Rx | THEVAF(R)84C | 8 MHz to 112 MHz |

## 3. Overview



## 2. Features

- Compatible with TIA/EIA-644 LVDS Standard
- 7:1 LVDS Transmitter and Receiver
- Single power supply
- Power Down Mode
< THEVAM83D>
$\cdot$ LVDS swing is reducible as 200 mV by RS-pin to reduce EMI and power consumption.
- Input clock triggering edge is selectable by R/F-pin
$<$ THEVAF84B and C $>\cdot$ Falling Edge Clock
$<$ THEVAR84B and C $>\cdot$ Rising Edge Clock

Figure 1 THEVAM83D and THEVAF(R)84B, 84C Top Side View

(a) THEVAF(R)84B, 84C (Bottom Side)

(b)THEVAF (R)84B, 84C (Bottom Side)

Figure 2 THEVAM83D and THEVAF(R)84B, 84C Bottom Side View

## 4. Power Supply Set Up

This chapter shows power supply condition.
Caution: Please check if there is no power-GND short on below red trace before supplying any power.

### 3.3V Power Supply to Each Board

Each evaluation board require 3.3 V power supply. Please use "CON1" connector typically.

(a)THEVAM83D

(b) THEVAF (R) 84B, 84C

Figure 3 Power Supply for Evaluation Board

## Power Supply from / to Connector

3.3 V power supply can be connected to Header1 and CON2 by using W1 and W2solder jumper.

## THEVAM83D

W1: Connect the 3.3 V power supply with pin\#29 and 30 of CON2.
W2: Connect the 3.3 V power supply with pin\#1, 2 and 3 of Header1.


Figure 4 THEVAM83D Power Supply from / to Each Connector

## THEVAF(R)84B, 84C

W1: Connect the 3.3 V power supply with pin\#1, 2 and 3 of Header1.
W2: Connect the 3.3 V power supply with pin\#1 and 2 of CON2.


Figure 5 THEVAF(R)84B, 84C Power Supply from / to Each Connect

## 5. Function Setting

Setting pin of each boards are shown in yellow area of figure 6. HEADER is connected to IC's setting pin. Each setting pin's high or low setting can set by connecting HEADER and high level or low level.


Figure 6 Position of Function Setting Pin


Figure 7 High / Low Setting Description

## 5. Function

This chapter shows function setting of THEVAM83D and THEVAF(R)84B, 84C.

Table 2 THEVAM83D Function Setting Description

| Silk | Symbol | Function |  |  |
| :---: | :---: | :---: | :---: | :---: |
| RF | RF | Input clock triggering edge select input for latching input data <br> H : Rising edge L: Falling edge |  |  |
| RS | RS | LVDS Swing Mode. |  |  |
|  |  | RS | LVDS Swing | Small Swing Input Support |
|  |  | VCC | 350 mV | N/A |
|  |  | 0.6 to 1.4 V | 350 mV | $\mathrm{RS}=\mathrm{V}_{\text {REF }}$ |
|  |  | GND | 200 mV | N/A |
|  |  | $\mathrm{V}_{\text {REF }}$ : is Input Reference Voltage |  |  |
| PWDN | PWDN | Power down input. <br> H: Normal operation L: Power down |  |  |

Table 3 THEVAF(R)84B, 84C Function Setting Description

| Silk | Symbol | Function |  |
| :---: | :---: | :---: | :---: |
| PWDN | PWDN | Power down input. <br> H: Normal Operation | L: Power Down |

## 6. Schematic



Figure 8 THEVAM83D Schematic


Figure 9 THEVAF(R)84B, 84C Schematic

## 7. Bills of Materials

Table 4 THEVAM83D BOM

| TYPE | Value / Part No. | Package | SPEC | Reference No. | Qty. |
| :--- | :--- | :--- | :--- | :--- | :---: |
| Capacitor | 10 uF | 2012 | 16 V | C1, C2, C3, C4 |  |
| Capacitor | 0.1 uF | 1005 | 16 V | C5, C6, C7, C12 | 4 |
| Capacitor | 0.01 uF | 1005 | 16 V | C8, C9, C10, C11 |  |
| Connector | $282836-2(\mathrm{NC})$ | 5 mm _pitch | 2 pin | CON1 |  |
| Connector | $52271-3069(\mathrm{NC})$ | 1 mm _pitch | 30 pin | CON2 | 4 |
| Connector | PCN10-48P-2.54DSA_LEFT(NC) | 2.54 mm pitch | 48 pin | Header1 | 1 |
| Header | 3HEAD(NC) | 2.54 mm pitch | --- | Header2, Header3, Header4 | 1 |
| IC | THC63LVDM83D | 1608 | --- | 1 |  |
| Inductor | MPZ1608R471A | 1608 | IC1 | 3 |  |
| LED0 | SML-310MT | 1005 | GREEN | D1, L2, L3 | 1 |
| Resistor | $150 \Omega$ | 0.1 W | R1 | 3 |  |

Table 5 THEVAF(R)84B, 84C BOM

| TYPE | Value / Part No. | Package | SPEC | Reference No. | Qty. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Capacitor | 10uF | 2012 | 16V | C1, C2, C3, C4 | 4 |
| Capacitor | 0.1uF | 1005 | 16 V | C5, C6, C7, C12 | 4 |
| Capacitor | 0.01uF | 1005 | 16 V | C8, C9, C10, C11 | 4 |
| Connector | PCN10-48P-2.54DSA_RIGHT(NC) | 2.54 mm _pitch | 48pin | Header1 | 1 |
| Connector | 52271-3069(NC) | 1 mm _pitch | 30pin | CON2 | 1 |
| Connector | 282836-2(NC) | 5 mm _pitch | 2pin | CON1 | 1 |
| Header | 3HEAD(NC) | 2.54 mm _pitch | --- | Header2 | 1 |
| IC | THC63LVDF(R)84B, 84C | TSSOP56 | --- | IC1 | 1 |
| Inductor | MPZ1608R471A | 1608 | 1.2A | L1, L2, L3 | 3 |
| LED0 | SML-310MT | 1608 | GREEN | D1 | 1 |
| Resistor | $150 \Omega$ | 1005 | 0.1W | R1 | 1 |
| Resistor | $100 \Omega$ | 1005 | 0.1W | R10, R15, R23, R28, R35 | 5 |
| Resistor | $10 \Omega$ | 1005 | 0.1W | $\begin{aligned} & \text { R2, R3, R4, R5, R8, R11, R13, R14, R16, } \\ & \text { R17, R18, R19, R21, R24, R26, R27, R29, } \\ & \text { R30, R31, R32, R33, R34, R36, R38, R39, } \\ & \text { R40, R41, R42, R43 } \end{aligned}$ | 29 |
| Resistor | $0 \Omega(\mathrm{NC})$ | 1005 | 1A | R6, R7, R9, R12, R20, R22, R25, R37 | 8 |

## 8. Set items

Table 6 Set Items

| TYPE | Part No. |
| :--- | :--- |
| DC Connector | $282836-2$ |
| FFC Connector for LVDS Link | $52271-3069$ |
| FFC 30pin 1mm pitch for LVDS Link | $98267-0475$ |
| Pin Header | --- |

It's possible to mount these parts on this board and use.
9. Notices and Requests

1. The product specifications described in this material are subject to change without prior notice.
2. The circuit diagrams described in this material are examples of the application which may not always apply to the customer's design. We are not responsible for possible errors and omissions in this material. Please note if errors or omissions should be found in this material, we may not be able to correct them immediately.
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